

APPLICATION UNDER UNITED STATES PATENT LAWS

Atty. Dkt. No. PW 0276915
(M#)

Invention: MICRO-CHIMNEY AND THERMOSIPHON DIE-LEVEL COOLING

Inventor (s): CHRYSLER, Gregory M.
MAVEETY, James G.

Pillsbury Winthrop LLP
Intellectual Property Group
1600 Tysons Boulevard
McLean, VA 22102
Attorneys
Telephone: (703) 905-2000

This is a:

- ☐ Provisional Application
- ☒ Regular Utility Application
- ☐ Continuing Application
 - ☐ The contents of the parent are incorporated by reference
- ☐ PCT National Phase Application
- ☐ Design Application
- ☐ Reissue Application
- ☐ Plant Application
- ☐ Substitute Specification
 - Sub. Spec Filed _____
 - in App. No. _____ / _____
- ☐ Marked up Specification re
 - Sub. Spec. filed _____
 - In App. No _____ / _____

SPECIFICATION

MICRO-CHIMNEY AND THERMOSIPHON DIE-LEVEL COOLING

BACKGROUND

1. Field

[0001] This invention relates generally to packaging for integrated circuits. More specifically, this invention relates to a method and arrangement for dissipating heat in an integrated circuit.

2. Background and Related Art

[0002] Integrated circuits generate heat to varying degrees. In typical applications, such heat must be dissipated to ensure that thermal effects do not impair the performance of, or even damage, integrated circuits. Thermal resistance is defined as temperature difference per unit power dissipated. When thermal resistance values are high, heat is not readily dissipated. In localized areas of a chip, such as a processor chip, heat fluxes may be very large, and temperatures may be very high. For instance, localized heat fluxes over the floating point area of a processor may be on the order of 350 W/cm^2 , and temperatures may rise as high as 130°C .

[0003] Various techniques have been employed to dissipate heat, including the attachment of thermal spreaders, integrated heat spreaders, heatsinks, or fans to integrated circuits. Such techniques involve external control of heat generated internally by integrated circuit components.

[0004] FIGs. 1A and 1B (Prior Art) are top and side views, respectively, of a structural arrangement 100. Arrangement 100 includes a semiconductor die 101 and a heat spreader 120 attached to a top face of die 101. Die 101, which may comprise a processor, includes local hot spots 110. Hot spots 110 may be associated with various

mechanisms internal to die 101, such as a floating point unit, which generate localized heat. In arrangement 100, localized heat is dissipated away from die 101 only after the heat diffuses to the surface thereof and then diffuses to heat spreader 120.

[0005] As processor performance continues to increase, so too does heat generated by processor components. Existing cooling methods may not dissipate sufficient heat to obviate the heat-related problems identified above.

[0006] Therefore, what is needed is a method and arrangement for dissipating heat in an integrated circuit.

BRIEF DESCRIPTION OF THE DRAWINGS

[0007] FIGs. 1A and 1B (Prior Art) are top and side views, respectively, of a structural arrangement that includes a semiconductor die and a heat spreader.

[0008] FIGs. 2A and 2B are top and side views, respectively, of a micro-chimney embodiment of the present invention.

[0009] FIG. 2C illustrates heat flow in a micro-chimney according to an embodiment of the present invention.

[0010] FIGs. 3A and 3B are top and side views, respectively, of a micro-chimney embodiment of the present invention.

[0011] FIGs. 4A and 4B are top and side views, respectively, of a micro-chimney embodiment of the present invention.

[0012] FIG. 4C illustrates heat flow in a micro-chimney according to an embodiment of the present invention.

[0013] FIGs. 5A and 5B are top and side views, respectively, of a thermosiphon embodiment of the present invention.

[0014] FIG. 5C illustrates heat flow in a thermosiphon according to an embodiment of the present invention.

[0015] FIGs. 6A and 6B are top and side views, respectively, of a thermosiphon embodiment of the present invention.

DETAILED DESCRIPTION

[0016] A method and arrangement for dissipating heat from a localized area within a semiconductor die, as disclosed herein, involve a semiconductor die that is constructed and arranged to have at least one conduit portion therein. At least a portion of the conduit portion is proximate to the localized area. The conduit portion is at least partially filled with a heat-dissipating material. The conduit portion absorbs heat from the localized area and dissipates at least a portion of the heat away from the localized area. As such, thermal stress on the die is reduced, and total heat from the die is more readily dissipated.

[0017] A. Micro-Chimney Embodiments

[0018] FIGs. 2A and 2B are top and side views, respectively, of a micro-chimney embodiment 200 including a semiconductor die 101 according to an embodiment of the present invention. Die 101, which is fabricated of silicon, includes one or more hot spots 110 associated with one or more mechanisms internal to die 101, such as a floating point unit. Hot spots 110 may correspond to areas in die 101 where heat generation is especially high. Die 101 also includes micro-chimneys 230.

[0019] Micro-chimneys 230 may comprise conduits, such as holes, channels, vias, slots, or tubes, located near hot spots 110. Micro-chimneys 230 may trace a path from hot spots 110 to various cooler spots in die 101, such as, for example, areas near a

surface of die 101. In exemplary implementations, micro-chimneys 230 may be laser-drilled or etched onto a back face of die 101. In embodiment 200 shown in FIGs. 2A and 2B, micro-chimneys 230 are cylindrical. Micro-chimneys 230 may extend vertically through die 101.

[0020] Micro-chimneys 230 may be filled with a material of high conductivity known in the art, such as copper or silver. As such, micro-chimneys 230 provide a highly conductive, low thermal resistive path to dissipate heat away from hot spots 110 to cooler spots in die 101. The heat ultimately may be absorbed and dissipated by an external device (not shown), such as a thermal spreader or integrated heat spreader located near cooler spots in die 101. According to embodiments of the present invention, because heat is more efficiently dissipated, temperature gradients within a semiconductor die are reduced, and a more uniform heat flux leaves the die. Thermal stress on the die is reduced, and total heat from the die is more readily dissipated. As such, processors may perform more reliably.

[0021] Micro-chimneys 230 may exist in parallel with existing silicon material in die 101 and need not intersect one another. Such a parallel construction provides a lower overall thermal resistance, resulting in lower thermal resistive paths for heat transfer. In other embodiments, various micro-chimneys may intersect, or be joined with, various other micro-chimneys.

[0022] FIG. 2C illustrates heat flow in micro-chimneys 230 of die 101. Heat 260 from a hot spot 110 is absorbed by micro-chimney 230. The highly conductive material in micro-chimney 230 dissipates the heat, transferring heat 270 to a cooler spot in die 101 and, in some embodiments, ultimately to a spreader.

[0023] FIGs. 3A and 3B are top and side views, respectively, of a micro-chimney embodiment 300. Semiconductor die 101 includes micro-chimneys 340 that

have a tapered, conical geometry. Such a geometry may provide enhanced spreading of heat in certain implementations.

[0024] FIGs. 4A and 4B are top and side views, respectively, of a micro-chimney embodiment 400. Semiconductor die 101 includes micro-chimneys 450 that have a T-shaped geometry. Micro-chimneys 450 may be staggered, such as shown in embodiment 400.

[0025] FIG. 4C illustrates heat flow in micro-chimneys 450 of die 101. Heat 480 from a hot spot 110 is absorbed by micro-chimney 450. The highly conductive material in micro-chimney 450 dissipates the heat, transferring heat 490 to a cooler spot in die 101 and, in some embodiments, ultimately to a spreader. As shown, the T-shaped geometry provides for enhanced lateral spreading of heat.

[0026] B. Thermosiphon Embodiments

[0027] FIGs. 5A and 5B are top and side views, respectively, of a thermosiphon embodiment 500 including a semiconductor die 101 according to an embodiment of the present invention. Die 101 includes thermosiphons 520.

[0028] Thermosiphons 520 may comprise conduits, such as holes, channels, vias, slots, or tubes located near hot spots 110. Thermosiphons 520 may trace a path from hot spots 110 to various cooler spots in die 101, such as areas near a surface of die 101. In some embodiments, thermosiphons 520 may be laser-drilled or etched onto a back face of die 101. In embodiment 500, thermosiphons 520 are cylindrical.

[0029] Thermosiphons 520 may be partially filled with liquid, such as, for example, water, dielectrics such as fluorocarbons, alcohols, or other fluids capable of changing phase. An embodiment employing water may exploit the latent heat of vaporization of water in order to dissipate heat. In particular, water at a lower end of a

thermosiphon 520 near hot spots 110 may evaporate due to high localized heat flux. Because the water vapor has a lower density and increased kinetic energy, the water vapor may travel to the upper end of thermosiphon 520. Heat is transferred to a cooler spot, and the water vapor condenses on the upper end and sides of thermosiphon 520. The water may then drip back to the lower end of thermosiphon 520 by the force of gravity. In other embodiments, because of the small characteristic length scales, surface tension forces may ensure that all surfaces remain wet regardless of their orientation.

[0030] FIG. 5C illustrates heat flow in thermosiphons 520 of die 101. Heat 540 from a hot spot 110 is absorbed by thermosiphon 520. Water in thermosiphon 520 evaporates in evaporation portion 560 of thermosiphon 520. Heat 550 is transferred through the upper end and sides of thermosiphon 520 to a cooler spot in die 101 and, in some embodiments, ultimately to a spreader. Water vapor condenses in condensation portion 570 of thermosiphon 520. Water flows back down the sides of thermosiphon 520.

[0031] As such, thermosiphons 520 provide low thermal resistive paths to dissipate heat away from hot spots 110 to cooler spots in die 101. The heat may ultimately be absorbed and dissipated by an external device (not shown), such as a thermal spreader or integrated heat spreader located near cooler spots in die 101. Because heat is more efficiently dissipated, temperature gradients within a semiconductor die are reduced, and a more uniform heat flux leaves a die. Thermal stress on the die is reduced, and total heat from the die is more readily dissipated.

[0032] In another embodiment (not shown), a thermosiphon may comprise a loop-shaped tube. Water and water vapor in the thermosiphon may flow in a generally clockwise or counterclockwise direction in the tube.

[0033] In other embodiments, one or more thermosiphons may include multiple channels, wherein at least one channel is surrounded by another channel. The channels may be partially connected. FIGs. 6A and 6B show top and side views, respectively, of such a thermosiphon embodiment 600. Thermosiphon 620 includes liquid return channels 660 within a thermosiphon channel 630. Each liquid return channel 660 may be connected to thermosiphon channel 630 at some location (not shown), such as ends of liquid return channels 660. Heat from a hot spot 110 is absorbed by thermosiphon 620. Water in thermosiphon 620 evaporates in evaporation portion 640 of thermosiphon 620. Heat is transferred to a cooler spot in die 101 and, in some embodiments, ultimately to a spreader. Water vapor condenses in condensation portion 650. Water flows back to evaporation portion 640 via liquid return channels 660.

[0034] The foregoing description of the preferred embodiments is provided to enable any person skilled in the art to make or use the present invention. Various modifications to these embodiments are possible, and the generic principles presented herein may be applied to other embodiments as well. For instance, a geometry for a micro-chimney or a thermosiphon need not conform to the geometries presented above. Various geometries, including irregularly shaped geometries, may be utilized. Further, multiple geometries may be employed in a single die. As such, the present invention is not intended to be limited to the embodiments shown above but rather is to be accorded the widest scope consistent with the principles and novel features disclosed in any fashion herein.